## <u>Department of Computer Science & Engineering</u> <u>Revised Course structure of B. Tech, CSE (session 2024-25)</u>

3 <sup>rd</sup> SEMESTER							
SL. NO.	PAPER CODE	PAPER NAME	L	Т	P	CONTACT HRs/WEEK	CREDIT
ТНЕОБ	THEORY						
01	BS(CS)307	Mathematics- III	3	0	0	3	3
02	PC(CS)301	Computer Organization	3	1	0	4	4
03	PC(CS)302	Data structure & Algorithms	3	1	0	4	4
04	PC(CS)303	Communication Engineering	3	0	0	3	3
05	HS(CS)305	Economics for Engineers	2	0	0	2	2
06	MC(CS)301	Environmental Sciences	2	0	0	2	2
SESSIONAL/PRACTICAL							
01	PCL(CS)304	Computer Organization Lab	0	0	3	3	1.5
02	PCL(CS)305	Data structure Lab	0	0	3	3	1.5
03	PCL(CS)306	IT Workshop (Python)	0	0	3	3	1.5
04	PCL(CS)307	Communication Engineering Lab.	0	0	3	3	1.5
05	TRN-SSD(CS)301	Soft Skill Development (intra institutional)	0	0	0	0	1
TOTAL	TOTAL			2	12	30	25

Name of the	ne course	Mathematics-III			
Course Code: BS(CS) 307		Semester: 3 <sup>rd</sup>			
Duration: 6 month		Maximum Marks: 100			
Teaching S		<b>Examination Schem</b>			
Theory: 3h		Mid Term 1 Exam: 1	5 Marks		
Tutorial: 01	nrs/week	Mid Term 2 Exam: 1:	5 Marks		
Practical: 0	hrs/week	Other Assessment too	ols		
		(Assignment, Quiz e	tc.): 20 Marks		
Credit Poir	its: 3	End Semester Exam:	100 Marks (5	50% weightage	
		for final reckoning i.e	e., 50 mark)		
<b>Objective:</b>					
1.	To understand the use of periodic signals				
2.	To understand gradient, divergence and co	url using the calculus	and multiple v	ariable.	
3.	To understand Green, Gauss and stokes th	neorem using integral of	of a function.		
4.	To learn analytical technique for finding s	solution of higher orde	er differential e	equation.	
5.	To create mathematical models using first	t order differential equ	iation.		
6.	To understand basic concept of graph the	ory.			
Pre-Requisite					
1.	Mathematics –I (BS(CS/IT)101				
2.	Engineering Mathematics (UG level)				
Unit	Content		Hrs	Marks	
1	Module 1: Fourier series & Fourier tran	sforms	8		
	Introduction to infinite series, convergence	_			
	Periodic functions, Dirichlet's condition. F				
	periodic functions with period 2 and arbitra				
	range Fourier series. Fourier transforms pro				
	Sine Fourier transforms, convolution, inver	rses and Parseval's			
	Identity.				
2	Module 2: Multivariable Calculus (Diffe	*	7		
	Limit, continuity and partial derivatives, C				
	function, Jacobian, Directional derivatives, Maxima, minima and saddle points; Gradie	•			
	divergence and related problems.	siit, cuit aiiu			
3	Module 3: Multivariable Calculus (Integ	retion)	8		
	Double and triple integrals (Cartesian and		O		
	order of integration in double integrals, Ch				
	(Cartesian to polar). Theorems of Green, G	_			
	(Statement only) and related problems.				
4	Module 4: Ordinary Differential Equation	on	7		
	First Order Differential Equation, Exact, L				
	Bernoulli's equations, Equations of first or				
	degree: equations solvable for p, equations				

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	equations solvable for x and Clairaut's form, general &					
	singular solution.					
	Second order linear differential equations with constant					
	coefficients, D-operator method, method of variation of					
	parameters, Cauchy-Euler equation.					
5	Module 5: Graph Theory	6				
	Basic Concept of graph, Walk, Path Circuit, Euler and					
	Hamiltonian graph, diagraph.					
	Matrix Representation: Incidence & Adjacency matrix.					
	Tree: Basic Concept of tree, Binary tree, Spanning Tree,					
	Kruskal and Prim's algorithm for finding the minimal					
	spanning tree.					
Course or						
After com	pletion of the course, a student would be able to:					
CO 1		Analyse circuits using periodic signals and Fourier series.				
CO 2	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	Understand the concept of limits, continuity and differentiability of functions of several				
	variables. Analytical definition of partial derivative. Maxima and minima of functions of					
several variables Define gradient, divergence and cur		scalar and vecto	or functions.			
CO 3	evaluate multiple integrals and apply the techniques to di	evaluate multiple integrals and apply the techniques to different physical problems.				
CO 4	solve first and second order ordinary differential equations by applying different					
	techniques and also will be able to formulate differential e	techniques and also will be able to formulate differential equations for model systems and				
	problems of engineering sciences.	i ü				
CO 5	apply the basic concepts of graph theory to network analysis, data analytics and many					
	other branches of computer science.					
Learning Resources:						
1.	S.K Mapa, Real Analysis, Sarat					
2.	Charles H.C. Little, Kee L. Teo, Bruce van Brunt, Real analysis via sequence and series,					
	Springer	-				
3. Douglas Brent West, Introduction to Graph Theory, Prentice Hall.						
4. Robert Wrede, Murray Spiegel, Schaum's Outline o			Third Edition,			
	Schaum's outline					
5.	S.L. Ross, Differential equation, Willey.					
6.	Clark John, Holton Derek Allan, A First Look at Graph T	heory, World S	cientific.			
community in the Book at Graph Theory, world before the						

Course Code: PC(CS)301   Semester: 3rd	Name of the cou	ırse	<b>Computer Organization</b>			
Duration: 6 months   Examination Scheme   Examination Scheme   Examination Scheme   Examination Scheme   Theory: 3 hrs/week   Mid Semester 1 Exam: 15 Marks   Tutorial: 1 hrs/week   Mid Semester 2 Exam: 15 Marks   Practical: 0 hrs/week   Assignment, Quiz, Attendance: 20 Marks   Credit Points: 4   End Semester Exam: 100 Marks (100 marks converted to 50)      Dipactive: 1						
Teaching Scheme   Examination Scheme   Theory: 3 hrs/week   Mid Semester 1 Exam: 15 Marks   Practical: 0 hrs/week   Mid Semester 2 Exam: 15 Marks   Practical: 0 hrs/week   Assignment, Quiz, Attendance: 20 Marks   Credit Points: 4   End Semester Exam: 100 Marks (100 marks converted to 50)	Duration: 6 months					
Tutorial: 1 hrs/week Practical: 0 hrs/week Credit Points: 4  Cobjective:  1						
Practical: 0 hrs/week Credit Points: 4  Credit Points: 4  Credit Points: 4  Chipective:  1 To understand the structure, function, and design of computers.  2 To study data representation, memory systems, different processor architecture and their performance measurement parameters.  3 To develop the concept of instruction set of a processor and control unit design.  4 To design pipeline processor architecture.  Pre-Requisite  1. Basic knowledge of Digital Logic.  Unit Content  1 Introduction: History of computing, von Neumann machine, Instruction and data, fixed-point and floating-point numbers, errors, IEEE standards  2 Processor design: Instruction Set Architecture-Instruction format, opcode optimization; operand addressing; Instruction implementation-data movement, branch control, logical, Input/output and debugging instructions; arithmetic instruction implementation-addition and subtraction, multiplication-division, 2's complement multiplication; Booth's algorithm—theory and examples; bit-pair algorithm; high performance arithmetic; Pipelining: Basic concepts, hazards, and performance  3 Control unit design: Hardwired control, micro-programmed control design — micro-instruction formats, control optimization;	0		Mid Semester 1 Exam: 15	Marks		
Credit Points: 4  End Semester Exam: 100 Marks (100 marks converted to 50)  Objective:  1			Mid Semester 2 Exam: 15	Marks		
Credit Points: 4  End Semester Exam: 100 Marks (100 marks converted to 50)  Objective:  1 To understand the structure, function, and design of computers. 2 To study data representation, memory systems, different processor architecture and their performance measurement parameters. 3 To develop the concept of instruction set of a processor and control unit design. 4 To design pipeline processor architecture.  Pre-Requisite 1. Basic knowledge of Digital Logic.  Unit Content Hrs Marks 1 Introduction: History of computing, von Neumann machine, Instruction and data, fixed-point and floating-point numbers, errors, IEEE standards 2 Processor design: Instruction Set Architecture-Instruction format, opcode optimization; operand addressing; Instruction implementation-data movement, branch control, logical, Input/output and debugging instructions; arithmetic instruction implementation—addition and subtraction, multiplication-division, 2's complement multiplication; Booth's algorithm—theory and examples; bit-pair algorithm; high performance arithmetic; Pipelining: Basic concepts, hazards, and performance 3 Control unit design: Hardwired control, micro-programmed control design — micro-instruction formats, control optimization;	Practical: 0 hrs/v	veek	Assignment, Quiz, Attend	lance: 20	Marks	
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Input/output and debugging instructions; arithmetic instruction implementation—addition and subtraction, multiplication-division, 2's complement multiplication; Booth's algorithm—theory and examples; bit-pair algorithm; high performance arithmetic; Pipelining: Basic concepts, hazards, and performance  Control unit design: Hardwired control, micro-programmed control design — micro-instruction formats, control optimization;		format, opcode optimization; operand addressing; Instruction				
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control design – micro-instruction formats, control optimization;						
optimization;	3			6		
			ction formats, control			
4   Memory subsystem: Registers, Memory technology, memory   9						
	4			9		
interfacing, Memory hierarchy-introduction to virtual						
memory system; cache memory – performance, address			=			
mapping, content addressable memory (CAM)				_		
Peripherals: Basic properties, bus architectures, interfacing of 7	5			7		
I/O devices, data transfer schemes –programmed I/O, DMA,			–programmed I/O, DMA,			
mass storage, RAID	<b>G</b>					
Course Outcomes: After completion of this course students will be able to-						
<b>CO1</b> Explain the evolution of computer systems and demonstrate understanding of numbers.	CO1	Explain the evolution of computer s	systems and demonstrate u	nderstan	ding of number	
systems, data representation, and IEEE floating-point standards.		<del>-</del>	<del>-</del>		C	

CO2	Design and analyze instruction sets, addressing modes, and arithmetic operations
	including Booth's and bit-pair multiplication algorithms.
CO3	Differentiate between hardwired and microprogrammed control units and apply
	microinstruction formats for control unit design.
CO4	Evaluate memory hierarchy and technologies including cache memory, virtual
	memory, and CAM, and their impact on system performance.
CO5	Analyze pipelined and superscalar processor architectures and assess their impact on
	instruction throughput and processor performance.
<b>Learning Resour</b>	rces:
1	Mano, M.M., "Computer System Architecture", PHI
2	Behrooz Parnami "Computer Architecture", Oxford University Press
3	Hayes J. P., Computer Architecture & Organization, McGraw Hill
4	Hamacher, Computer Organization, McGraw Hill,
5	N. Senthil Kumar, M. Saravanan, S. Jeevananthan, Microprocessors and
	Microcontrollers OUP
6	Chaudhuri P. Pal, Computer Organization & Design, PHI
7	P N Basu- Computer Organization & Architecture, Vikas Pub

Name of t	he course Da	ata structure & Alg	gorithm		
Course Code: PC(CS)302		Semester: 3 <sup>rd</sup>			
Duration: 6 months		Maximum Marks: 100			
Teaching	Scheme Ex	xamination Scheme	<b>;</b>		
Theory: 3	hrs/week M	id Semester 1 Exam	: 15 Mark	S	
Tutorial: 1	hrs/week M	id Semester 2 Exam	: 15 Mark	S	
Practical: (	) hrs/week As	ssignment, Quiz, Att	endance:	20 Marks	
Credit Poir		nd Semester Exam: 1 50)	00 Marks	s (100 marks converted	
<b>Objective:</b>	·				
1.	Introduce fundamental data structures like a and demonstrate their use in software development	opment.	-		
2.	Develop students' ability to analyze algorith asymptotic notations.				
3.	Enable design and implementation of funda	mental operations of	n various	data structures.	
4.	Develop problem-solving skills using approsorting, and traversals.	priate data structure	s for tasks	s such as searching,	
5. Lay the groundwork for further study in algorithms, softwar			esign, and	systems development.	
Pre-Requi	isite				
1.	Programming for problem solving (ES(CS/I	(T) 204)			
Unit	Content		Hrs	Marks	
1	Module 1: Introduction to Algorithms and Definition and characteristics of algorithms, specification, pseudocode, Time and space of Asymptotic notations $(O, \Omega, \Theta)$ , Recurrence introduction)	Algorithm omplexity,	4		
2	Module 2: Linear Data Structures Arrays and Strings: One-dimensional and multi-dimensional arra and complexity analysis, String representation manipulation  Linked Lists: Singly, doubly, and circular linked lists, Opedelete, search, traverse, Memory management allocation	ys, Operations on and erations: insert,	10		
	Stacks and Queues:				

	Stack operations and applications: postfix evaluation,					
	recursion					
	Queue types: simple, circular, priority queues					
	Implementations using arrays and linked lists					
3	Module 3: Non-Linear Data Structures	10				
	Trees:					
	Tree terminology and binary trees, Binary Search Trees					
	(BST),					
	<b>Traversal methods-</b> in-order, pre-order, post-order (recursive					
	and iterative)					
	<b>BST operations-</b> insertion, deletion, search					
	Introduction to balanced trees (concept of AVL tree only)					
	Heaps:					
	Min-heap and max-heap, Heap operations and applications,					
	Heap sort					
	Graphs:					
	Graph terminology and representations (adjacency					
	matrix/list), Breadth-First Search (BFS) and Depth-First					
	Search (DFS)					
4	Module 4: Searching and Sorting Algorithms	8				
	Searching:					
	Linear search, Binary search, Analysis of efficiency					
	Sorting:					
	Bubble sort, Selection sort, Insertion sort, Merge sort, Quick					
	sort, Heap sort, Time and space complexity analysis and					
	comparisons					
5	Module 5: Hashing and File Structures	4				
	Hash functions and collision resolution (chaining, open					
	addressing)					
	Introduction to file organization and indexing					
Course outo	comes					
After comple	etion of the course, a student would be able to:					
CO 1	Explain the concepts of algorithms, time and space compl	Explain the concepts of algorithms, time and space complexity, asymptotic notations, and				
	recurrence relations.					
CO 2	Implement linear data structures such as arrays, linked lists, stacks, and queues to perform					
	basic operations.					
CO 3		Construct non-linear data structures including trees, heaps, and graphs using appropriate				
	representations.					
CO 4	• 1	analyse the performance of searching and sorting algorithms using time and space				
	complexity.					
<u> </u>	I .					

CO 5	Demonstrate the use of hashing and basic file organization techniques for efficient data access.			
Learning Reso	ources:			
1.	Horowitz, Sahni, Anderson-Freed: Fundamentals of Data Structures in C (Second Edition), Universities Press, 2008.			
2.	Lipschutz, Seymour. Data Structures (Schaum's Outline Series). Tata McGraw-Hill Publishing Company Ltd., New Delhi.			
3.	T.H. Carmen, C.E. Leiser son, R. Rivest and C. Stein: Introduction to Algorithms, (Second/Third Edition), PHI, 2009.			
4.	Aho, Alfred V., Hopcroft, John E., and Ullman, Jeffrey D. Data Structures and Algorithms. Reading, MA: Addison-Wesley, 1983. ISBN: 978-0201000238.			
5.	R. Sedgewick: Algorithms in C, Pearson, 2004.			
6.	Steven S Skeena, Algorithm design manual, 2nd Edition, Springer.			
7. Steven S Skeena, Miguel A. Revilla, Programming Challenges: The Programming Cont Training Manual (Texts in Computer Science) Springer.				

Name of the course:		Communication Engineering	
Course Code: PC(CS)303  Duration: 6 months		Semester: 3 <sup>rd</sup>	
		Maximum Marks: 100	
Teaching Scheme		Examination Scheme	
Theory Contact Hrs.: 3 hrs./week		Mid Semester-1 Exam: 15 Marks	
Tutorial Contact Hrs.	:	Mid Semester-2 Exam: 15 Marks	
Credit Point: 3		Assignment, Quiz & class Attendance: 20 Marks	
		End Semester Exam: 100 Marks (to be mapped into 50 marks)	
Objective:			
1.	To study Amplitude Modulation and Frequency Modulation techniques		

2.	To study pulse modulation techniques and line codes.					
3.	To study different shift keying techniques					
4.	To study different aspects of satellite communication					
Pre-Requisite:						
1.	Mathematics I BS(CS/IT) 101, Mathematics II BS(CS/IT) 205, Digital Electronics [ES(CS/IT)307					
Unit	Content	Hours	Marks			
1.	Introduction to Communication Engineering, need of Modulation, Amplitude Modulation (AM): Concept of AM, Calculation of Modulation Index, total transmitted power of AM, DSB-SC modulation & SSB-SC modulation techniques, calculation of Bandwidth and Savings of power, Demodulation of AM, Superheterodyne Receiver					
2.	Frequency Modulation (FM): Concept of FM, Direct & Indirect Method, Bandwidth calculation of FM, Demodulation of FM. Phase Modulation (PM): Concept of PM, generation of PM from FM.					
3.	Pulse & Digital Communication: Sampling Theorem, aliasing effect, natural and flat top sampling, PAM, PWM, PPM, basic concept of Pulse Code Modulation (PCM), concept of quantization and quantization error, Compounding, DPCM, Delta Modulation and Adaptive Delta Modulation, signal to quantization noise ratio in PCM system. ASK, FSK, PSK, QPSK					
4.	Data Formatting: NRZ-Unipolar, NRZ-polar, NRZ-Bipolar, RZ-Bipolar, Manchester Coding, Synchronous and Asynchronous Data Transmission, Concept of Satellite Communication  07					
Course Outcomes:						
After completion of this course the students will be able to -  Explain the necessity of Modulation and how to transfer information from one place to another place using Amplitude Modulation, Frequency Modulation and Phase Modulation.						
CO2	Apply the concept of sampling and quantization for analog to digital signal conversion.					
CO3	Compare various techniques of digital communication techniques.					
CO4	Compare different line coding techniques.					
CO5	Compare Satellite Communication system with terrestrial communication system.					
Learning Resources:		-				
1.	Modern Digital and Analog Communication Systems by B.P. Lathi, Published by Oxford University Press.					
2.	An Introduction to Analog and Digital Communications by Simon Hay kin (Wiley India)					
3.	Principles of Communication Engineering by Taub H. & Shilling D	.L TMH				

4.	Introduction to Digital and Data Communication – Michael A. Miller, Jaico Publishing House
5.	Communication Systems by A. B. Carlson, Published by McGraw-Hil
6.	Principles of Analog and Digital Communication by Jerry D Gibson, Published by MacMillan.
7.	A Text Book of Analog and Digital Communication by A Kumar, Umesh Publication
8.	Communication Systems (Analog and Digital) by Sanjay Sharma, Published by S.K. Kataria& Sons
9.	Modern Electronic Communication, Principles and Practice- Sharma &Sinha, Dhan Patrai Publishing Company (p) Ltd

Name of the cour	Name of the course Economics for engineers				
Course Code: HS	S(CS)305	Semester: 3 <sup>rd</sup>			
<b>Duration: 6 mon</b>	ths	Maximum Marks: 100			
<b>Teaching Schem</b>	e	<b>Examination Scheme</b>			
Theory: 2 hrs./we	eek	Mid Term Exam I: 15 M	arks		
Tutorial: Nil		Mid Term Exam II: 15 M	Iarks		
Practical: Nil		Assignment.: 20 Marks			
Credit Points: 2		Semester End Exam: 100	Marks (Two	o third	
		weightage for final recko	ning i.e., 50	marks)	
<b>Objective:</b>					
1.	To familiarize with the analysis of differen		ing appropri	ate	
	concepts and methods of Economics for E	ngineers.			
<b>Pre-Requisite</b>					
1.	Class 12th standard knowledge of Mathem	atics.			
Unit	Content		Hours	Marks.	
1	<ul> <li>Introduction to Economics fo</li> </ul>	r Engineers – Basic	7		
	Introduction to Economics, Produ	ctive resources, Scarcity			
	and the Economic Problem,				
	possibility frontier, Demand and	11 0			
	income, Forms of Business Organiz				
	needs, Basic Financial Statements				
	Economics, Definition and Sco	-			
	Engineers, Role of Engineers in Eco	onomic Decision making,			
	Decision-Making Process.				
	<ul> <li>Engineering Cost Concepts and Co</li> </ul>				
	– Fixed, Variable, Marginal & Ave	•			
	and Step cost, Product and Period				
	cost, Sunk cost, Shutdown cost, Op				
	and Nonrecurring costs, Anticip				
	costs, Differential or Incremental of	costs, Cash cost vs. Book			

	<ul> <li>costs, Explicit and Implicit costs, Out of pocket costs and Imputed costs, Life-Cycle Costing. Approaches to cost estimation, Types of Estimate, Cost Estimation Models - Per Unit Model, Segmenting Model, Cost Index Model, Power-Sizing Model, Learning Curve Model, Benefits and difficulties in estimation.</li> <li>Break-even analysis- Basic concept, terminology and assumptions, Derivation of break-even point, Profit Volume (P/V) ratio, Margin of Safety, Uses and limitations of break-even analysis.</li> </ul>		
2	<ul> <li>Cash Flow, Interest and Equivalence: Cash Flow – Diagrams and Cash Flow Statement, Time Value of Money, Real, Nominal &amp; Effective Interest rate. Different Interest Formulae and their application.</li> </ul>	3	
3	<ul> <li>Capital budgeting and Project selection – Basic concept of capital budgeting, Net Present Value (NPV) Analysis, Internal Rate of Return (IRR) Analysis, Incremental analysis, Future Worth Analysis, Payback period, Evaluation of Public Projects and Benefit-Cost Ratio Analysis, Sensitivity analysis</li> </ul>		
4	<ul> <li>Uncertainty in Future Events - Uncertainty and Risk, Types of risk, Risk vs. Return, Application of Probability to analyse risk, Using Expected Value, Variance, and Coefficient of Variation to measure return and risk; Economic Decision Trees.</li> <li>Inflation and Price Change – Definition, Stages, Effects, Causes, Price Change with Indexes, Types of Indexes, Composite vs Commodity Indexes, Tests for Index numbers, Use of Price Indexes in Engineering Economic Analysis.</li> </ul>	5	
5	<ul> <li>Depreciation and Replacement Analysis - Basic aspects of depreciation, Depreciation, depletion and amortization, Various methods of calculating depreciation; Replacement analysis - Basic aspects, Types of maintenance, Replacement Analysis Decision Map, Minimum Cost Life of a New Asset.</li> </ul>	3	
Course outcomes			
	of the course, a student would be able to:		
CO 1	Analyse short term alternatives using basic principles of Economics, Cost estimation and break-even analysis.		
CO 2	Examine the concept of Interest and Equivalence.		
CO 3	Inspect the feasibility of projects using various methods like Net Pres Worth, Internal rate of Return and Benefit Cost Analysis.	sent Worth, I	Future
CO 4	Identify the effects of uncertainty and price change.		
CO 5	Apply Depreciation and Replacement Analysis using appropriate merproblems.	thods in rele	vant
<u></u>			

<b>Learning Resou</b>	rces:
1.	R. Panneerselvam: Engineering Economics, PHI.
2.	H.L. Bhatia & S.N. Maheswari: Economics for Engineers, second edition, Vikas Publishing
	House Pvt Ltd.
3.	Donald Newnan, Ted Eschenbach, Jerome Lavelle: Engineering Economics Analysis, OUP
4.	Sullivan and Wicks: Engineering Economy, Pearson
5.	Partha Chatterjee: Economics for Engineers, Vrinda Publications.
6.	James L. Riggs, David D. Bedworth, Sabah U. Randhawa: Economics for Engineers 4e, Tata McGraw-Hill.
7.	Dr. Shantanu Chakraborty, Nilanjan Singha Roy: Economics for Engineers, Law point
7.	Publications.
8.	N.G. Das, Statistical Methods, Tata McGraw Hill
9.	Chan S Park, Contemporary Engineering Economics, Parson

Name	e of the course	ENVIRONMENTAL SCIENCES		
Cour	se Code:	Semester: 3 <sup>rd</sup>		
<b>Duration: 6 months</b>		Maximum Marks: 100		
Teach	ning Scheme	Examination Scheme		
Theor	ry: 2hrs/week	Mid Semester-1 Exam: 15 Marks		
Tutor	ial: NIL	Mid Semester-2 Exam: 15 Marks		
Practi	cal: NIL	Assignment, Quiz & class attendance: 20 Marks		
Credit Points: 2		End Semester Exam: 75 Marks (Two third weightage for fina reckoning i.e., 50 marks)		
Objec	tive:			
1.	To provide knowledge about green comp	outing and green strategies		
2.	To learn about the various types of E waste and their composition, their harmful effects on the environment.			
3.	To know about carbon footprint, quantification and measurement			
4	To get the idea about urban problems rela	ted to energy and to know about sustainable transportation.		
5	To learn various green technologies and t	heir implementation's.		

	equisite 12 and 14 and 15 and		
	12 standard knowledge of physics, chemistry, biology, mathematic		
Unit	Content	Hrs	Marks
1	Green computing: Green IT Fundamentals, business, IT and the environment, Green IT strategies; Drivers, Dimensions and Goals	4	
2	Green Assets and Modelling; Buildings, Data Centres, Networks and Devices, green enterprise architecture, Environmental intelligence –green supply chains	4	
3	Carbon footprint: definition, quantification, measurement, direct and indirect emissions, types, difference from Ecological Footprint.	3	
4	E waste: definition, global scenario, growth of Electrical and Electronics industry in India E waste generation in India, environmental and health impact of E waste	5	
5	Social issues and the Environment	4	
	(a) Urban problems related to energy		
	(b) Water conservation, rain water harvesting, water shed management		
	(c) Resettlement and rehabilitation of people; its problems and concerns,		
	(d) Climate change, global warming, acid rain, ozone layer depletion, nuclear		
6	Green technologies; a) renewable energy (solar, wind etc.), energy efficiency	4	
	b) waste management (recycling, composting)		
	c) sustainable transportation (electric vehicles, biofuels)		
	d) green building design and pollution control		
	e) green chemistry (carbon capture and storage)		
	f) sustainable agriculture		

CO 1	Understand the environmental consequences of information technology and the importance of green computing
CO 2	Understand the components environmental and health impact of e waste
CO 3	Apply the strategies of e-waste management in real world scenarios.
CO 4	Analyse the environmental consequences of various technologies and industrial processes.
CO 5	Apply their knowledge in implementing policies that incentivize sustainable practices, regulate emissions and promote carbon offsetting initiatives.
CO6	apply their knowledge of various environment protection acts, "Environment Impact Assessment" (EIA) as and when required in setting up of new industries a well as expansion of industries in which they will be employed
arning Re	sources:
1	Anubha Kaushik, C.P. Kaushik, Perspectives in environmental studies, New Age International (P) Ltd, Publishers
2	Erach Bharucha, Textbook for Environmental Studies, University Grants Commission
3	D. D. Mishra, Fundamental concepts in Environmental Studies, S Chand & Co L
4	Anil Kumar De, Arnab Kumar De, Environment and Ecology, New age international (P) Limited, Publishers
5	Environmental Chemistry by Anil Kumar De, Wiley Eastern Limited
5	Environmental Chemistry by Anil Kumar De, Wiley Eastern Limited  Linda D. Williams, Environmental Science demystified, McGraw-Hill

Name of the co	urse Co	mputer Org	anization L	ab.
Course Code: 1	PCL(CS)304 Ser	Semester: 3 <sup>rd</sup>		
Duration: 6 months		Maximum Marks: 100		
Teaching Scheme		Examination Scheme		
Theory: Nil	Att	endance: 10		
Tutorial: Nil	Pre	Preparation of Lab Report: 30		
Practical: 3 hrs.	/week Exp	Experimental data/ Precision of work done: 30		
Credit Points: 1	.5 Pre	esentation/ an	alysis of the	e result: 10
	Viv	va Voce: 20		
Pre-requisite:				
1.	Digital Electronics laboratory (ESL(CS/IT)	308)		
Module	Content		Hours	Marks
1.	Truth table verification of Universal gate. implementation of basic gate using Universal gate.	_	03	
2.	Design and implementation of binary addenader.	er using full	06	
3.	Design and implementation of increment of half adder.	circuit using	03	
4.	Design and implementation of BCD adder adder.	using binary	03	
5.	Design and implementation of adde composite unit.	er-subtractor	03	
6.	Design and implementation of carry look a adder.	head (CLA)	03	
7.	Design and implementation of 4:1 / 8:1 MU MUX.	JX using 2:1	03	
8.	Design and implementation of ALU.		03	
9.	Design and implementation of syncasynchronous counter.	chronous /	03	
Course Outcom	nes:			
After completion	n of this course the students will be able to -			
CO1	Design and verify the functionality of bas gate-level circuit implementations.	sic and unive	ersal logic g	gates through truth tables and
CO2	Design and simulate binary adders and sub-	tractors.		
CO3	Design multiplexers (MUX) of higher ord	er using lowe	er order MU	X.
CO4	Design and implement an ALU.			

CO5	Develop and compare synchronous and asynchronous counters.	
Learning Resources:		
1	Mano, M.M., "Computer System Architecture", PHI.	
2	M. Lotia, Modern IC data and substitution Manual, PHI	

Name o	of the course Data Structure & Algorithm Lab			
		Semester: 3 <sup>rd</sup>	<b>9</b>	
	on: 6 months	Maximum Marks: 10	00	
Teachir	ng Scheme	<b>Examination Scheme</b>		
Theory:	Theory	Attendance: 10		
Tutorial	: Nil	Preparation of Lab Rep	port: 20	
Practica	ıl: 3	Experimental data/ Pre	ecision of work done: 3	30
Credit F	Points: 1.5	Presentation/ analysis	of the result: 20	
		Viva Voce: 20		
Objecti	ve:			
1.	<b>Hands-on Implementation:</b> Develop	practical skills in imple	menting data structures	s and algorithms.
2.	<b>Problem-Solving:</b> Reinforce problem-solving abilities through the design and implementation of efficien algorithms.			
3.	Analysis of Algorithmic Efficiency: Understand and evaluate algorithmic complexity using both time and space metrics.			xity using both time and
4.	<b>Algorithmic Applications:</b> Gain experience with the application of different data structures to solve real-world problems.			ctures to solve real-world
5.	<b>Debugging and Optimization:</b> Learn to debug, optimize, and test data structure implementations.			elementations.
Pre-Re	<u> </u>			
1.	Basic Programming concept using C			
Modul	Content		Hours	Marks.
1 1	Introduction & Basic Programming programming environment, I/O operation memory management. Introduce algorical Array Operations: Implement operation deletion, traversal, and searching on array performance with large data. String Operations: Implement basic strict concatenation, comparison, searching, extraction.	ons, and dynamic athm complexity.  ons such as insertion, rays. Test  ing operations such as	6	

2	Singly Linked List: Implement singly linked list with	3		
2	operations such as insertion, deletion, and traversal.	3		
3	<b>Doubly Linked List:</b> Implement doubly linked list with	3		
3	operations such as insertion, deletion, and traversal.			
4	Stack Operations: Implement a stack using arrays and	3		
	linked lists. Use stacks for applications like postfix			
	expression evaluation.			
5	Queue Operations: Implement simple queue, circular	3		
	queue, and priority queue. Perform enqueue and dequeue			
	operations.			
6	<b>Binary Tree Operations:</b> Implement binary tree with insert,	3		
	delete, and traversal operations (pre-order, in-order, post-			
	order).			
7	<b>Binary Search Tree (BST):</b> Implement BST with	3		
	operations like insert, delete, search, and traversal. Evaluate			
	efficiency of each operation.			
8	<b>Graph Representation and Traversal:</b> Implement graph	3		
	representation using adjacency matrix/list. Perform BFS and			
	DFS traversal.	_		
9	Searching and Sorting Algorithms: Implement searching	3		
	algorithms (linear, binary) and sorting algorithms (bubble,			
	insertion, quick, merge).			
	Heap Operations: Implement min-heap and max-heap.			
Comman	Perform heap sort and evaluate its time complexity.  outcomes			
	mpletion of the course, a student would be able to:			
CO 1	Demonstrate proficiency in using basic programming construction	et I/O operations debu	aging dynamia	
COI	memory allocation, and algorithmic complexity analysis while	<u> </u>		
CO 2	Implement array and string operations including insertion, dele			
	large datasets.	etion, scarcining, and pe	criormance testing on	
CO 3	Develop linear data structures such as singly and doubly linked	d lists stacks and quer	ies ilsing appropriate	
	data representations.	a noto, stacks, and quee	ses using appropriate	
CO 4	Construct non-linear data structures such as binary trees, binary	y search trees, heaps, a	and graphs with relevant	
	traversal and manipulation algorithms.	, 1	C 1	
CO 5	Evaluate the efficiency of different searching and sorting algor	rithms.		
CO 6	Analyse the performance of different searching and sorting alg	gorithms in different sc	enarios.	
Learnin	g Resources (Books):			
1.	Aho, Alfred V., Hopcroft, John E., and Ullman, Jeffrey D. Dat	ta Structures and Algor	rithms	
	Addison-Wesley, 1983. ISBN-13: 978-0201000238			
2.	Horowitz, Ellis, Sahni, Sartaj. Fundamentals of Data Structure	es in C Pearson, 2nd $\overline{Ed}$	lition, 2006. ISBN-13:	
	978-8177584724, ISBN-10: 8177584727			
3.	Cormann, Thomas H., Leiser son, Charles E., Rivest, Ronald I		Introduction to	
	Algorithms, MIT Press, 3rd Edition, 2009. ISBN-13: 978-0262			
4.	S. Lipschutz, Data Structures, Schaum's Outline Series, McGraw-Hill, 2006. ISBN-13: 978-0070634136			
Softwar	e and Tools			

1	IDE/Compiler: Code: Blocks, Eclipse, or Visual Studio Code (C/C++/Java programming), GCC, Turbo
	C++, or Clang (for C/C++ programming)
Online	Resources
1	Geeks for Geeks: https://www.geeksforgeeks.org/data-structures/
2	Khan Academy: https://www.khanacademy.org/computing/computer-science/algorithms
3	Coursera (University of California, San Diego): <a href="https://www.coursera.org/specializations/data-structures-">https://www.coursera.org/specializations/data-structures-</a>
	<u>algorithms</u>

Name of the course	IT	T Workshop (Python).		
Course Code: PCL(	CS)306 Se	emester: 3 <sup>rd</sup>		
<b>Duration: 6 months</b>	M	Maximum Marks: 100		
<b>Teaching Scheme</b>	E	Examination Scheme		
Theory: Nil	A	Attendance: 10		
Tutorial: Nil	Pı	reparation of Lab Report: 30	)	
Practical: 3 hrs./week	E	Experimental data/ Precision	of work don	e: 30
Credit Points: 1.5	Pı	resentation/ analysis of the r	result: 10	
	V	iva Voce: 20		
<b>Objective:</b>				
1.	To implement Python programs using core		cepts and fur	nctions
2.	To understand Object Oriented Python Pr	rogramming technique		
Pre-Requisite				
1.	Basic Programming concept			
Module	Content		Hours	Marks.
1	Python Fundamentals		6	
	Python Character Set, Python Tokens, I	<u> </u>		
	Program, Variables and assignments,	1 0		
	Dynamic Typing, Input and Output in	• • •		
	Operators, Control Structure, Sequence	· ·		
	Statements, range () function, Iterative Sta	atements, Jump Statements		
2	Strings		3	
	Accessing Values in Strings, Traversing a	a String, String Operators,		
2	Built-In String Methods		4	
3	Lists	1 · G · IT ·	4	
	Creating a List, Accessing Lists, Difference	ce between String and List,		
	Traversing a List, List Operations,			
4	Tuples		3	
	Tuple vs List, creating a Tuple, Access	sing Tuples, traversing a		
	Tuple, Comparing Tuples, Common Tuple			
	Unpacking Tuples, Tuples Built-In Functi	ions, Deleting a Tuple		
5	Dictionary		3	

	Creating a Dictionary, Properties of Dictionary Keys, traversing a		
	Dictionary, Accessing Keys or Values Separately, Nested		
	Dictionary, Adding Elements to Dictionary, Updating Elements in a		
	Dictionary, Deleting Element from a Dictionary, Dictionary Built-In		
	Methods		
6	Introduction to Python Modules	3	
	Math Module, Random Module, Statistics Module.		
7	Functions	3	
	Scope, Parameter passing, passing strings, Default parameters,		
	Return values, Positional parameters		
8.	<b>Object Oriented Programming (OOP) With Python</b>	5	
	Basics of OOP, Class and Objects, Inheritance, Types of Inheritance		
Course outcomes			
After completion of t	he course, a student would be able to:		
CO 1	Interpret the fundamental Python syntax and semantics and be fluent in the use of Python		
	control flow statements		
CO 2	Express proficiency in the handling of strings and functions		
CO 3	Apply object-oriented programming concepts		
CO 4	Determine the methods to create and manipulate Python programs by utilizing lists, tuples		
	and dictionaries		
Learning Resources			
1.	https://www.anaconda.com		
2.	2. Rakesh K. Yadav, Srinivas Aru Konda, Monu Singh, Tapasya Din	kar, Dileep	Kumar
	Yadav, Zero to Mastery in		
3.	Python Programming, Vayu Education of India, ISBN: 97893897693	364	
4.	3. Pooja Sharma, Programming in Python, BPB Publications, ISBN:	9789386551	276

Course Code: PCL(CS)307		Communication Engineering Lab.  Semester: 3 <sup>rd</sup> Maximum Marks: 100							
					Teaching S	cheme	<b>Examination Scheme, T</b>	otal Ma	rks: 100
					Theory: Ni	1	Attendance: 10		
Tutorial: Ni	1	Preparation of Lab Report: 30							
Practical: 3	hrs./week	Experimental data/ Precision of work done: 30							
Credit Poin	ts: 1.5	Presentation/ analysis of the result: 10							
		Viva Voce: 20							
Module Content			Hours	Marks					
1.	Amplitude Modulation and Demodula	ution	04						
2.	Frequency modulation and Demodulation.		04						

3.	Generation and Detection of PAM	03				
4.	Generation and detection of PWM & PPM	06				
5.	Generation and detection of ASK	03				
6.	Generation and detection of FSK	04				
7.	Time Division Multiplexing & Demultiplexing	06				
Course	Outcomes:	·				
After co	mpletion of this course the students will be able to -					
CO1	Compare the Amplitude modulated (AM) and Frequency	Compare the Amplitude modulated (AM) and Frequency modulated (FM) signals.				
CO2	Measure the modulation index of amplitude modulated and frequency modulated signals.					
CO3	Compare PAM, PWM and PPM signal.					
CO4	Compare ASK and FSK signals with AM and FM signals.					
CO5	Identify the multiplexed signals at the output of TDM system and the corresponding demultiplexed signals at the receiver end.					
Learnin	g Resources:					
1	Octave online <a href="https://octave-online.net/">https://octave-online.net/</a> the open-source experiments	alternative for	simulation of the above			

Name of the course	So	oft Skill			
Course Code: TRN-SSD(CS)301		Semester: 3 <sup>rd</sup>			
<b>Duration: 6 months</b>	M	Iaximum Marks: 100			
Teaching Scheme	Ex	xamination Scheme			
Theory: Theory	At	ttendance: 10			
Tutorial: Nil	Pro	reparation of Lab Repor	t: 20		
Practical: 3	Ex	xperimental data/ Precis	ion of work	done: 30	
Credit Points: 1.5	Pro	resentation/ analysis of t	the result: 20	)	
	Vi	iva Voce: 20			
Objective:					
1.	Development of intra and Interpersonal Skills of Students.				
2.	To enhance Personal and Professional Growth	th of Student.			
3.	Enable the students to develop their communi	nicative competence.			
<b>Pre-Requisite</b>					
Module	Content		Hours	Marks.	
1	Introduction and personality development	t	4		
What are soft skills					
	Need for soft skills				
	Time Management: Goal setting, effective time utilization.				
	Self-awareness: SWOT ANALYSIS				
2	Communication and public speaking		5		

	Email etiquette, writing clear and concise messages.		
	➤ <b>Group discussion</b> Public Speaking: Overcoming stage fright,		
	structuring speeches, and engaging an audience.		
	<b>Presentation Skills</b> : Designing visually appealing slides,		
	delivering impactful presentations		
	,Non-verbal Communication: Body language, facial expressions,		
	and gestures		
3	Emotional Intelligence and Self-Confidence Understanding	6	
3	Emotional Intelligence: Managing emotions, empathy, and self-		
	regulation.		
	<b>Developing Self-Confidence</b> : Overcoming self-doubt, positive		
	self-talk, and body language.		
	Stress Management: Techniques to handle stress and maintain		
	emotional well-being.		
	g .		
	Adaptability and Resilience: Dealing with change and challenges		
	effectively.  Problem solving and Critical thinking		
1	Problem solving and Critical thinking Interpersonal Skills and Professional Etiquette	4	
4	-	4	
	> Teamwork and Collaboration: Effective participation in		
	teams, conflict resolution, and cooperation.		
	➤ <b>Networking Skills:</b> Building and maintaining professional		
	relationships.		
	➤ <b>Business Etiquette</b> : Proper behaviour in a professional setting,		
	dressing appropriately.		
5.	Interview Skills: Preparing for interviews,	4	
6.	Pronunciation practice. Practical usage of English grammar.	3	
Course outcomes	1 0 0 0	3	
After completion	of the course, a student would be able to:		
CO 1	Students will be able to differentiate between hard skills and soft sk	tills. By the o	completion of
	this unit students will understand the importance of interpersonal sk	ills in today'	s competitive
	world.		
CO 2	Ctudents will know shout their self assessments and negociality. Ct	vidanta vivill 1	20442
CO 2	Students will know about their self-assessments and personality. St		
	negotiation skills and also, they will learn the importance of social	_	nake the
	world a nicer place with simple social etiquette gestures and good r	nanners.	
CO 3	Students will learn how to communicate effectively through various	s advanced a	ctivities
	specifically designed to encourage students to play an active role for		
	knowledge and developing learning strategies.		
	knowledge and developing learning strategies.		
	I		

CO 4	Students will have learnt how to make decisions when in dilemma, how to deal with complex
	problems and stress in the present world. They will also have learnt how to be a team player
	& how to develop leadership skills.
CO 5	After the completion of this unit students' will have learnt about how to develop positive
	attitude, how to work on Emotional Intelligence. They will also have learnt about the strategies
	to set crystal clear goals, how to think independently and innovatively.
Learning Resource	es:
1.	Anderson, P. (2017). Technical Communication (9th ed.). Cengage Learning.
2.	Seely, J. (2013). Oxford Guide to Effective Writing and Speaking: How to Communicate
	Clearly (3rd ed.). Oxford University Press.
3.	Bovee, C., & Thill, J. (2017). Business Communication Today (14th ed.). Pearson.
4.	L. (2004). Basic Business Communication: Skills For Empowering the Internet Generation
	(10th ed.). McGraw-Hill/Irwin

	4 <sup>th</sup> SEMESTER						
SL. NO.	PAPER CODE	PAPER NAME	L	Т	P	CONTACT HRs/WEEK	CREDIT
THEORY							
01	BS(CS)408	Discrete Mathematics	3	0	0	3	3
02	PC(CS)408	Design & Analysis of Algorithm	3	1	0	4	4
03	PC(CS)409	Formal Language and Automata Theory	3	1	0	4	4
04	PC(CS)410	Computer Architecture	3	1	0	4	4
05	PC(CS)411	Operating Systems	3	1	0	4	4
SESSIONA	AL/PRACTICAL						
01	PCL(CS)412	Algorithm Lab	0	0	3	3	1.5
02	PCL(CS)413	Programming Lab using C++	0	0	3	3	1.5
03	PCL(CS)414	Operating System Lab	0	0	3	3	1.5
04	PCL(CS)415	Computer Architecture Lab	0	0	3	3	1.5
05	PROJ(CS)401	Minor Project 1 (Literature Survey)	0	0	2	2	1
TOTAL			15	4	14	33	26

Name of t	he course	Discrete mathemati	cs		
		Semester: 4 <sup>th</sup>			
	Duration: 6 month Maximum Marks: 100				
	hing Scheme Examination Scheme				
Theory: 3		Mid Term 1 Exam:	15 Marks		
Tutorial: 1		Mid Term 2 Exam: 1			
Practical:	0 hrs/week	Other Assessment to			
		(Assignment, Quiz e	etc.): 20 Marks		
Credit Poi	nts: 3	End Semester Exam:		50% weightage	
		for final reckoning i.e	e., 50 mark)		
Objective	:	7			
1.	To learn the concept of division algorithm	n and integer modulo	n.		
2.	To understand counting techniques and co	ombinatorics in the co	ntext of discre	te probability.	
3.	To learn recurrence relations and generati	ng functions.			
4.	To learn a given logic sentence and can cl				
5.	To understand Algebraic structures and cl	•	on		
6.	To understand basic concept of graph the	•			
Pre-Requ		ory, Duar and planar g	51 <b>u</b> pii.		
1.	Mathematics –I (BS(CS/IT)101, Mathematics	atics-III(BS(CS/IT)30	07)		
2.	Engineering Mathematics (UG level)	wite 111(22(e2/11)ee	. , ,		
Unit	Content		Hrs	Marks	
1	Module 1: Theory of Numbers:		7		
	Principles of Mathematical Induction, Wel	l Ordering Principle,			
	Divisibility theory and properties of divisib				
	theorem of Arithmetic; Euclidean Algorith				
	and some basic properties of G.C.D with s	_			
	Congruence, Residue classes of integer mo	odulo n (Zn) and its			
	examples, Chinese Remainder Theorem.				
2	Module 2: Counting Techniques:		6		
	Pigeon- hole Principle, Principles of inclus	sion and exclusions;			
	Recurrence relations: Formulation & Mode	elling of different			
	counting problems in terms of recurrence r				
	linear recurrence relations with constant co	` •			
	second order) by (i) The iterative method (				
	roots method (iii) Generating functions method.				
3	Module 3: Propositional Logic:		7		
	Syntax, Semantics, Validity and Satisfiabil	•			
	Connectives and Truth Tables, Logical Equ				
	of Logic, Logical Implication, Rules of Inf				
	Quantifiers. Proof Techniques: Some Term				
	Methods and Strategies, Forward Proof, Pr				
	Contradiction, Proof by Contraposition, Pr	<del>_</del>			
4	Sufficiency. Disjunctive and Conjunctive r		10		
4	Module 4: Algebraic Structures and Mo	rphism:	10		

	Algebraic Structures with one Binary Operation, Semi Groups,				
	Monoids, Groups, Congruence Relation and Quotient				
	actures, Permutation Groups, Normal Subgroups, Quotient				
	group, Homomorphism & Isomorphism (Elementary				
	properties only). Algebraic Structures with two Binary				
	Operation, Rings, Integral Domain and Fields. Boolean algebra				
	and Boolean Ring, Identities of Boolean Algebra, Duality,				
	Representation of Boolean Function.				
5	Module 5: Graph Theory: 6				
	Planar and Dual Graphs. Kurowski's graphs. Homoeomorphic				
	graphs. Eulers formula $(n - e + r = 2)$ for connected planar				
	graph and its generalisation for disconnected graphs. Detection				
	of planarity. Graph colouring. Chromatic numbers of simple				
	graphs. Chromatic Numbers and its bounds, Independence and				
	Clique Numbers, Perfect Graphs-Definition and examples,				
	Chromatic polynomial and its determination, Applications of				
Graph Colouring. Simple applications of chromatic numbers.					
	Statement of four and five colour theorems.				
Course ou					
	pletion of the course, a student would be able to:				
CO 1	<b>determine</b> multiplicative inverses, modulo n and use to solve linear congruences.				
CO 2	solve different engineering problems using counting techniques.				
CO 3	<b>express</b> a given logic sentence in terms of predicates, quantifiers, and logical				
	connectives and derive the solution for a given a problem using deductive logic and				
	prove the solution based on logical inference.				
CO 4	classify the algebraic structure for a given mathematical problem and evaluate Boolean				
	functions and simplify expressions using the properties of Boolean algebra.				
CO 5	<b>develop</b> the given problem as graph networks and solve with techniques of graph				
theory.					
Learning Resources:					
1. C L Liu and D P Mohapatra, Elements of Discrete Mathematics A Computer On					
Approach, 3rd Edition by, Tata McGraw – Hill.					
2.	N. Chandrasekaran and M. Uma Parvathi, Discrete Mathematics, PHI.				
3.	J.K. Sharma, Discrete Mathematics, Macmillan.				
<b>4.</b> Malik, Margeson, Sen, Fundamentals of abstract algebra, Tata McGraw-Hill.					
5.	Clark John, Holton Derek Allan, A First Look at Graph Theory, World Scientific.				

Name of the course	Design and Analysis of Algorithm
Course Code: PC(CS)408	Semester: 4 <sup>th</sup>
Duration: 6 months	Maximum Marks: 100
Teaching Scheme	Examination Scheme
Theory: 3 hrs./week	Mid Term Exam I: 15 Marks

Tutorial: N	II. Mid	Term Exam II: 15 Marks		
	Practical: NIL Assignment.: 20 Marks			
Credit Poin		ester End Exam: 75 Marks (Ty	Two third weightage	
Credit I on		nal reckoning i.e., 50 marks)		
Objective:		mai reckoning nei, co marks)		
1.	To understand different paradigms of algori	thms such as greedy, dynamic	nrogramn	ning.
1.	divide and conquer etc			
2.	To calculate the time complexities of algorit			
3.	The ability to decide based on a given probl		d algorith	n is
J.	appropriate	om which design paradigm an	a argorran	11 15
Pre-Requi				
1.	Data Structure and Algorithm (PC(CS)302)			
Module	Content		Lecture	
			Hours	
1	Non-comparison-based sorts - Radix sort, co	ount sort; Median order	6	
	statistics; Lower bound of sorting.			
2	Algorithm design frameworks - Divide/Deci	rease and Conquer,	10	
	Backtracking, Greedy, Dynamic Programmi	ng, Decision and		
	Optimization problems; Comparison - Divid	le & Conquer, Greedy and		
	Dynamic Programming.			
	Illustrations of various design framework:			
	Dynamic Programming - Optimal substructu			
	problems; Matrix-chain multiplication; Bacl			
	problem; Greedy Method - Knapsack proble	m, Job sequencing with		
	deadlines.			
3	Disjoint set data structure.		6	
4	Graph Algorithms: BFS and DFS- algorithm	•	10	
	and Kruskal's algorithms for finding minimu			
_	source shortest path, All pair shortest paths;		4	
5	Deterministic and non-deterministic algorith		4	
	Intractable problems, Solvability, P and NP:			
	NP-hard, NP-complete; reduction (concept of	only); Cook's theorem		
Course Out	(statement only).			<u> </u>
	comes: letion of this course, the learners will be able to	) <del>-</del>		
CO1	Implement non-comparison-based sorts	·		
CO2	1	Analyze a problem to determine which design paradigm to use to solve the problem.		
CO <sub>2</sub>	Apply disjoint set data structures			
CO4	Solve various graph problems efficiently.			
CU4	Solve various graph problems efficiently.			

CO5	Identify whether a problem is in P or NP					
Learning R	Learning Resources:					
1	T. H. Cormann, C.E. Leiser son, R. L. Rivest and C. Stein, "Introduction to Algorithms",					
	PHI.					
2	Ellis Horowitz, Sartaj R. Sahani, "Fundamentals of Computer Algorithms". Computer					
	Science Press.					
3	A. Aho, J. Hopcroft and J. Ullman, "The Design and Analysis of algorithms", Pearson					
	Education.					
4	D.E. Knuth: The Art of Computer Programming, Vol. 1, Vol. 2 and Vol. 3, Addison-					
	Wesley.					
5	G. Brassard, P. Bratley, Fundamentals of Algorithmics -, PHI.					
6	S. Baase, Allen Ven Gelder "Computer Algorithms-Introduction to Design & Analysis" - 3 <sup>rd</sup>					
	Edition, Pearson Education					

Name of the course		Formal Language and Automata Theory			
Course Co	de: PC(CS)409 Se	Semester: 4 <sup>th</sup>			
<b>Duration:</b>	6 month M	Maximum Marks: 100			
Teaching Scheme Exa		<b>Examination Scheme</b>			
Theory: 3h	Theory: 3hrs/week Mid Term 1 Exa				
Tutorial: 1h	rs/week M	id Term 2 Exam: 15	Marks		
Practical: 0	hrs/week Ot	her Assessment tool	ols		
		Assignment, Quiz etc			
Credit Poin		nd Semester Exam:		,	
	fo	r final reckoning i.e.,	, 50 mark	<u>(</u> )	
<b>Objective:</b>					
1.	Introduce formal models of computation and	the concept of langua	age recog	gnition.	
2.	Develop a theoretical foundation for underst	anding the limits of	computat	ion.	
3.	Analyze grammars, automata, and computat	ional complexity hie	rarchies.		
4.	Familiarize students with undecidable problem	ns and the capabilities	es of Tur	ing Machines.	
Pre-Requis	ite				
1.	Discrete Mathematics				
2.	Mathematical Reasoning and Problem Solvi	ng			
Unit	Content		Hrs	Marks	
1	Module 1: Mathematical Foundations and Set theory, relations, functions, formal proofs		7		
	contradiction, counterexamples), Alphabets, s	_			
	languages, grammar formalism, Chomsky hie				
	Deterministic and Non-Deterministic Finite A	<u>-</u>			
	NFA, ε-NFA), Finite Automata with output (	· ·			
	Mealy),				
	Conversions: NFA $\rightarrow$ DFA, $\epsilon$ -NFA $\rightarrow$ DFA,				
	Minimization of FA, Myhill-NE rode Theore				
2	Module 2: Regular Languages and Express		7		
	Regular Expressions (RE) and FA, Algebraic				
	Arden's Theorem, Constructing RE from FA				
	Pumping Lemma for RL, Applications Equiv				
	RE, Regular grammars and conversion betwe regular grammars,	CII KE AIIU			
	Closure properties, Decision problems				
	Closule properties, Decision problems			L	

3	Module 3: Context-Free Grammars and Languages	7	
	Context-Free Grammars (CFG): definition, derivation, parse		
	trees,		
	Ambiguity, left recursion, left factoring,		
	Simplification of CFGs: useless symbols, null and unit		
	productions		
	Normal forms: CNF, GNF		
	Closure properties, Pumping Lemma for CFLs		
	Decision problems for CFLs		
	Relationship between CFG and Regular Languages		
4	Module 4: Pushdown Automata	6	
	Introduction and formal definition, Instantaneous descriptions		
	and transitions, Acceptance by empty stack and final state,		
	Equivalence between PDA and CFG, Deterministic PDA and		
	Non-deterministic PDA, Two-stack PDA		
5	Module 5: Turing Machines and Undecidability	9	
	Basics and formal definition of TMs, Transition diagrams and		
	instantaneous descriptions, Variants of TMs: Multi-tape,		
	NDTM, Universal TM, TM as an integer function, LBA,		
	Unrestricted grammars, Recursive and Recursively		
	Enumerable Languages, Undecidability and Reducibility,		
	Post's Correspondence Problem (PCP), Modified PCP		
Course o	outcomes		

Course outcomes		
After completion	n of the course, a student would be able to:	
CO 1	Understand and construct formal machine models and grammars.	
CO 2	Analise the recognition power and equivalence of finite automata and regular expressions.	
CO 3	Design and simplify context-free grammars and apply parsing techniques.	
CO 4	Model and simulate pushdown automata for context-free languages.	
CO 5	Analise Turing machines and understand the concept of decidability and undecidability.	
Learning Resou	irces:	
1.	Hopcroft, Motwani, Ullman – Introduction to Automata Theory, Languages and Computation, Pearson	
2.	Peter Linz – An Introduction to Formal Languages and Automata, Jones & Bartlett	
3.	Martin, J.C. – Introduction to Languages and the Theory of Computation, McGraw Hill	

4.	Mishra, Chandrasekaran – Theory of Computer Science: Automata, Languages and
	Computation, PHI
5.	Michael Zipser – Introduction to the Theory of Computation, Cengage

Name of the course		Computer Architecture			
Course	Code: PC(CS) 410	Semester: 4th			
Duratio	on: 6 months	Maximum Marks: 1	ximum Marks: 100		
Teachir	Ceaching Scheme Examination Scheme				
Theory:	3 hrs./week	Mid Term I: 15 Marks			
Tutorial	: 1 hrs/week	Mid Term II: 15 Mar	·ks		
Credit F	Points: 4	Assignment, Test based on assignments, Surprise			
	to	ests, Quizzes, Presen	tations, etc. :	20 Marks	
	H	End Semester Exam:	50 Marks		
Objecti	ves:				
1.	To identify different processor architectures and their performance measurement parameters.				
2.	To apply different techniques for improving	the performance of	processor.		
3.	To develop the concept of multiprocessor architecture.				
4.	To design pipeline processor architecture.				
Pre-Re	quisites:				
1.	Digital Electronics [ES(CS/IT)307]				
2.	Computer Organization [PC(CS/IT)301]				
Unit	Content		Hrs	Marks	
1	Pipelining Architecture: Introduction:	Review of basic	9	30	
	computer architecture (Revisited), Quantita	ative techniques in			
	computer design, measuring and repor	ting performance.			
	Pipelining: Basic concepts, instruction and				
	data hazards, control hazards and structural	•			
	for handling hazards. Exception handling. Pi	peline optimization			
	techniques.				
2	Instruction-level parallelism: Basic conce	1	9	20	
	increasing ILP, RISC Architecture, supersca				
	and VLIW processor architectures. Array and				
3	Multiprocessor architecture: taxonor	•	9	30	
	architectures; Centralized shared-mem	•			
	synchronization, memory consistency,				
	networks. Distributed shared-memory are				
	computers. Non von Neumann architecture				
		tectures, systolic			
	architectures				

4	Memory Module: Hierarchical memory technology: 9 20				
	Inclusion, Coherence and locality properties; Cache memory				
	organizations, Techniques for reducing cache misses, cache				
	mapping techniques; Virtual memory organization.				
Cours	e Outcomes:				
After c	completion of this course students will be able to-				
CO1	Explain the concept of pipeline architecture, different hazards and analyze different				
	techniques for handling pipeline hazards				
CO2	Describe and differentiate non-von Neumann architectures such as data flow, reduction, and				
	systolic architectures and their applications in parallel computation.				
CO3	Classify and compare different multiprocessor architectures, interconnection networks, and				
	synchronization mechanisms in shared and distributed memory systems.				
CO4	Evaluate instruction-level parallelism and compare architectural approaches such as RISC,				
	superscalar, super pipelined, and VLIW for exploiting ILP.				
CO5	Analyse memory hierarchy designs including cache and virtual memory systems, and apply				
	techniques to reduce memory access latency and cache misses.				
Learn	ing Resources:				
1.	Advanced Computer Architecture-Kai Hwang & Naresh Jot Wani, McGraw Hill				
2.	Computer Architecture and Parallel Processing -Kai Hwang and A. Briggs, McGraw Hill				
3. Computer Architecture: a quantitative approach - J. L. Hennessy and D. A. P					
<b>3.</b>	Computer Architecture, a quantitative approach - 3. L. Heiniessy and D. A. I atterson,				
3.	Harcourt Asia, Singapore.				
4.	Harcourt Asia, Singapore.				
	Harcourt Asia, Singapore.  Computer Organization and Architecture - V. Rajaraman and T. Radhakrishnan PHI Learning				

Name of the course		Operating systems		
Course Code: PC(CS)411		Semester: 4 <sup>th</sup>		
	\ /	Maximum Marks: 100		
Teaching Scheme Examination Scheme				
Theory: 3 hrs./week Mid Term I Exam: 15 mar		TS.		
		Mid Term II Exam: 15 mark		
		Assignment, Test based on a		
Crount 1		tests, Quizzes, Presentations		
		End Semester Exam: 100 N		
Object	ive:			
1.	To understand and analyze operating system stru	ictures and services.		
2.	To understand and determine Process manageme			
3.	To understand and determine Memory management		Operatir	g System.
4.	To analyze and assess Disk management, I/O ma			
	System.	8		• F8
Pre-Re				
1	Computer Organization – PC(CS)301			
2	Data Structures & Algorithms - PC(CS)302			
Unit	Content		Hrs	Marks
1	<b>Introduction of O.S:</b> Concept of OS. Operating	system services, dual-	4	
	mode operation, Evaluation of O.S, Different typ	•		
	programmed, timesharing, real-time, distributed,	· · · · · · · · · · · · · · · · · · ·		
		luction of Process: Concept of process, Process life cycle,		
	Resource utilization, Operations on processes, IP			
2	System Structure: Computer system operation,	Operating system	4	
	structure, kernel: microkernel, monolithic kernel,	, system calls.		
	Threads: Overview, Benefits of threads, User an	nd kernel threads,		
	multithreading models.			
3	<b>CPU Scheduling:</b> Scheduling criteria, Preemptiv		10	
	scheduling, Scheduling algorithms (FCFS, SJF/S	SRTF, RR, Priority),		
	MLQ scheduling, multi-processor scheduling.			
	<b>Process Synchronization:</b> Race condition, Critic	cal Section problem,		
	Semaphore, Mutex, Monitor.			
	<b>Deadlocks:</b> Deadlock criteria, Methods for hand			
	allocation graph, Banker's algorithm, Recovery f			
4	Memory Management: Background, Logical vs		8	
	Address binding, Swapping, Contiguous memory	y allocation,		
	Fragmentation, Segmentation, Paging.	_		
	Virtual Memory: Concept, Demand paging, Pag	ge replacement, Page		
	replacement algorithms (FCFS, LRU, Optimal).	T211		
	File Systems: File attributes, File system structur			
	File allocation methods (contiguous, linked, inde	·		
5	<b>Disk Management:</b> Disk structure, Disk formatt	<i>U</i>	3	
	blocks, Disk scheduling algorithms (FCFS, SSTF	F, SCAN, C-SCAN,		
	LOOK, C-LOOK).			

6	I/O Management: I/O hardware, Polling, Interrupts, DMA, Application 7	
	I/O interface, Kernel I/O subsystem, Spooling and device reservation.	
	Protection & Security: Goals of protection, Security problem,	
	Authentication, Program threats, System threats	
Course	Outcome: After completion of the course students will able to	
CO1	analyse different types of operating system.	
CO2	Select different types of kernels in operating system	
CO3	Apply different mechanism to handle process management	
CO4	Determine different memory management, file management mechanism to provide better performance to users	
CO5	Evaluate different disk management policies	
CO6	Implement different techniques for protection and security.	
Learnir	ng Resources	
1	Operating System concepts- A. Silberschatz, Greg Gagne, and Peter Baer Galvin- Wiley India	
2	Operating Systems: Internals and Design Principles-William Stallings-Pearson	
3	Operating Systems Concepts & design - Milan Milenkovic, TMH	
4	Tanenbaum A.S. and Woodhull "Operating System Design & Implementation", Pearson	
5	Advanced Concepts in operating Systems - Mukesh Singhal and Niranjan G. Shivaratri, TMH	
6	Operating System Dhamdhere: - TMH	
7	An Introduction to Operating Systems- Dietel H. N- Addison Wesley.	

~		Algorithm Lab			
Course Code: PCL(CS)412		Semester: 4 <sup>th</sup>			
Duration: 6 months		Maximum Marks: 100			
Teaching Scheme Examination Scheme					
Theory: NIL Attendance 10					
Tutorial:	NIL	Preparation of Lab Report: 30			
Practical	:3 hrs/week	Experimental data/Precision of work	done: 30		
Credit Point: 1.5 Presentation/ analysis of the result: 10		)			
		Viva Voce:20			
Objectiv	ve:				
1.	To understand the working of Fundar	mental algorithms such as sorting.			
2.		thms based on the underlying data stru	ctures		
3.	To implement various graph algorithm				
4.	To decide which algorithms to emplo	by based on nature of problem.			
Pre-Req		-			
1.	NA NA				
Unit	Content		Hours	Marks	
1	Comparison of performance of various sorting algorithms. 03				
2	Implementation of median order statistics in O(n) time		03		
3	Performance comparison of problem solving using dynamic 06				
	programming and recursion.				
4	Implement disjoint set data structure and compare the performance 03				
	based on different data structures used.				
6	Implementation of BFS and DFS both recursive and non-recursive 03				
	version and their performance comparison.				
7		n and performance comparison based	03		
0	on different data structures used				
8	Implementation of Dijkstra's algorithm and performance comparison based on different data structures used				
9	Implementation of Bellman Ford al		03		
	algorithm	gorithm and an pair shortest paur	03		
10	Implementation of Network flow 03				
	Outcomes:		1		
	mpletion of this course, the learners wi	ill be able to-			
CO1	Compare performance of various sorting algorithm.				
CO2	Decide which design paradigm to use for a particular problem				
CO3	Implement various graph algorithms				
CO4	Apply graph algorithms to real life problems				

CO5	Compare performance of recursion and dynamic programming	
Learning Resources:		
1.	T.H. Carmen, C.E. Leiser son, R. Rivest and C. Stein: Introduction to Algorithms,	
	(Second/Third Edition), PHI, 2009.	
2.	R. Sedgewick: Algorithms in C, Pearson, 2004.	
3.	Steven S Skeena, Algorithm design manual, 2 <sup>nd</sup> Edition, Springer.	

Name of the Course	Programming Lab using C++
Course Code: PCL(CS)413	Semester: 4 <sup>th</sup>
<b>Duration: 6 months</b>	Total Marks: 100
Teaching Scheme	<b>Examination Scheme</b>
Theory: Nil	Attendance: 10 marks
Tutorial: Nil	Preparation of Lab Report: 30 marks
Practical: 3 hrs/week	Experimental data/ Precision of work done:
Practical: 5 firs/week	30 marks
Credit Points:1.5	Presentation / analysis of the result: 30 marks
	Viva voce: 20 marks
Objective	

- 1. To learn the object-oriented programming paradigm
- 2. To incorporate data security or protections issues in object-oriented programming through C++
- 3. To implement data abstraction scenario through C++ programming
- 4. To implement dynamic resolving of method call scenarios at run time situations
- 5. To handle run time exceptional scenarios through C++ programming

## Prerequisite

C programming

**Detailed Syllabus** 

Module	Content	Hrs	Marks
1	Basic idea of object-oriented programming, basic C++ programme with class, object, I/O streams, constructor, destructor, function overloading, copy constructor, identification of variables & methods with scope resolution operator, alias variable concepts,		
2	Dynamic memory allocation for variables and objects, array of objects, pointer to class data members, functions and objects	3	
3	Dealing with static variables & methods, static and non- static inner classes, local classes	6	
4	Friend functions- partial & full friend declarations, operator overloading – binary and unary operator overloading, operator overloading with friend and class member function	6	
5	Inheritance, base & derived class, hierarchical, multiple, multilevel inheritance with constructor calling sequence, derived class constructor declaration	3	
6.	Virtual base class concept, implementation of function overriding, static and dynamic resolving of function calls, pointers to base & derived classes, virtual functions and run time polymorphism, abstract class implantation	3	
7.	Basic concepts of exception handling with try/catch, class and function templates	3	

<b>Course outcomes</b>		
After completion of the course, a student would be able to:		
CO 1	<b>Demonstrate</b> C++ program with class, object and constructor concepts	
CO 2	<b>Apply</b> different data & class member protection approaches through C++ programming	
CO 3	<b>Apply</b> inheritance and various abstraction concepts through C++ programming	
CO 4	<b>Implement</b> various types of polymorphisms through C++ programming	
CO 5	<b>Design</b> effective C++ program codes by taking care of exceptional scenarios and template concepts	

Name of the course Course Code: PCL(CS)414 Duration: 6 months		Operating System Lab Semester: 4 <sup>th</sup> Maximum marks:100									
						Teaching Scheme		Examination scheme:			
						Theory: Nil		Attendance: 10 marks			
Tutorial: Nil		Preparation of Lab Report: 30 marks									
Practical: 3 hrs./week		Experimental data/ Precision of work done: 30 marks									
Credit Points:1.5		Presentation / analysis of the result: 30 marks									
		Viva voce: 20 marks									
Unit	Content	Content		Marks							
1.	Familiarization of Linux Commands.	Familiarization of Linux Commands.									
		Shell in UNIX. Different types of Shell in UNIX.									
2.	Creating a bash shell script, making a script executable,										
	shell syntax (variables, conditions, control structures, functions, commands).										
3.	Implementation of CPU scheduling algorithms.		3								
4.	Implementation of classical problems in process synchronization.		6								
5.	Implementation of deadlock handling techniques.		6								
6.	Implementation of memory management ted	Implementation of memory management techniques.									
7.	Operations on Processes, signals, Pipes and system calls.		3								
Course Outcomes	:	•									
After completion of	of the course students will able to -										
CO1	Review commands in UNIX.										
CO2	Write programs using shell scripts.	Write programs using shell scripts.									
CO3	Implement different process management mechanisms.										
CO4	Implement different memory management techniques.										
CO5	Evaluate different system management mechanisms.										

Name of the course		Computer .	Computer Architecture Lab			
Course Code: PCL(CS)415		Semester: 4	Semester: 4 <sup>th</sup>			
Duration: 6 months		Maximum	Maximum marks:100			
Teaching Scheme		Examination	Examination scheme:			
Theory: Nil		Attendance:	Attendance: 10 marks			
Tutorial: Nil		Preparation	Preparation of Lab Report: 30 marks			
Practical: 3 hrs./week		Experiment	Experimental data/ Precision of work done: 30 marks			
Credit Points:1.5		Presentation	Presentation / analysis of the result: 30 marks			
		Viva voce: 2	Viva voce: 20 marks			
Unit	Content		Hours	Marks		
1.	Introduction to Logisim / HDL		03			
2.	Basic Logic Gates and Combinatio	nal Circuits	Circuits 06			
3.	Arithmetic Circuits		03			
4.	Counter design					
5.	Register design	Register design				
6.	Memory unit design and perform n	emory operations. 03				
7.	Simple ALU design	Simple ALU design				
8.	Simple CPU design	Simple CPU design				
9.	Interfacing of CPU and Memory	Interfacing of CPU and Memory				
Course Outcom			1			
	on of this course the students will be able to		. 1	- 4 4 4		
CO1	encoders in Logisim.	<b>Construct</b> truth tables and simulate circuits using logic gates, adders, multiplexers, and encoders in Logisim.				
CO2		<b>Design</b> registers and counters, and understand their role in data storage and control logic.				
CO3	Build functional ALUs capable of	Build functional ALUs capable of performing basic arithmetic and logical operations.				
CO4	Implement simple memory units a	Implement simple memory units and understand data flow within computational systems.				
CO5	Integrate ALU, memory, control using Logisim.	<b>Integrate</b> ALU, memory, control logic, and registers to build and simulate a basic CPU using Logisim.				
Learning Resou	rces:					
1	Octave online <a href="https://octave-onlineabove">https://octave-onlineabove</a> above experiments	<u> </u>				